



深圳市一众显示科技有限公司

SHEN ZHEN TEAM SOURCE DISPLAY TECH. CO, LTD.

TFT-LCD Module Specification

Module NO.: TST035QVIH-28DP

Version: V1.0

APPROVAL FOR SPECIFICATION

APPROVAL FOR SAMPLE

For Customer' s Acceptance:	
Approved by	Comment

Team Source Display:		
Presented by	Reviewed by	Organized by

Version No.	Date	Content	Remark
V1.0	2018-6-2	Initial Release	

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1 General Characteristics

ITEM	Specification	Unit
LCD Type	a-Si TFT, Transmissive, Normally white, TN	-
LCD Size	3.5	inch
Resolution	320 x (RGB) × 240	pixel
LCM (W × H × D)	76.9(W) x 63.9(H) x 4.47(D)	mm
Active Area (W × H)	70.08(W) x 52.56 (H)	mm
Pixel size (W × H)	0.219*0.219	mm
Viewing Direction	12 o'clock	-
Gray Scale Inversion Direction	6 o'clock	-
Viewing Angle	Top:50, Bottom:40; Left/ Right:60	deg.
Color Depth	16.7M	-
Pixel Arrangement	RGB-stripe	-
Backlight Type	6 LED, 20mA, 18.6V	-
Surface Luminance	500	cd/m ²
LCD Surface Treatment	Anti-Glare	-
Polarizer	0.22mm, EWV	-
Driver IC	HX8238-D	-
Interface Type	RGB24-bit	-
Input Voltage	3.0 ± 0.3	V
TP/Lens	With RTP	-
Weight	30	g

Note 1: RoHS compliant

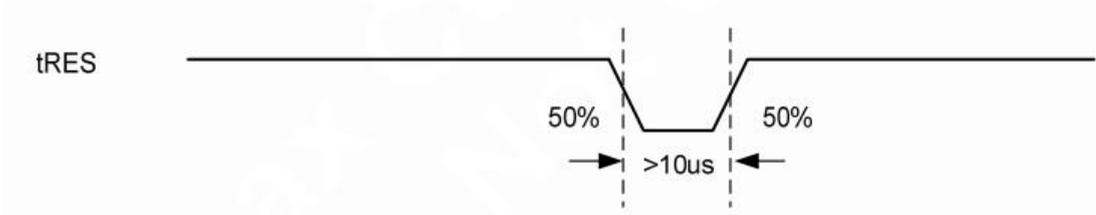
Note 2: LCM weight tolerance: ± 5%.

3 Interface description

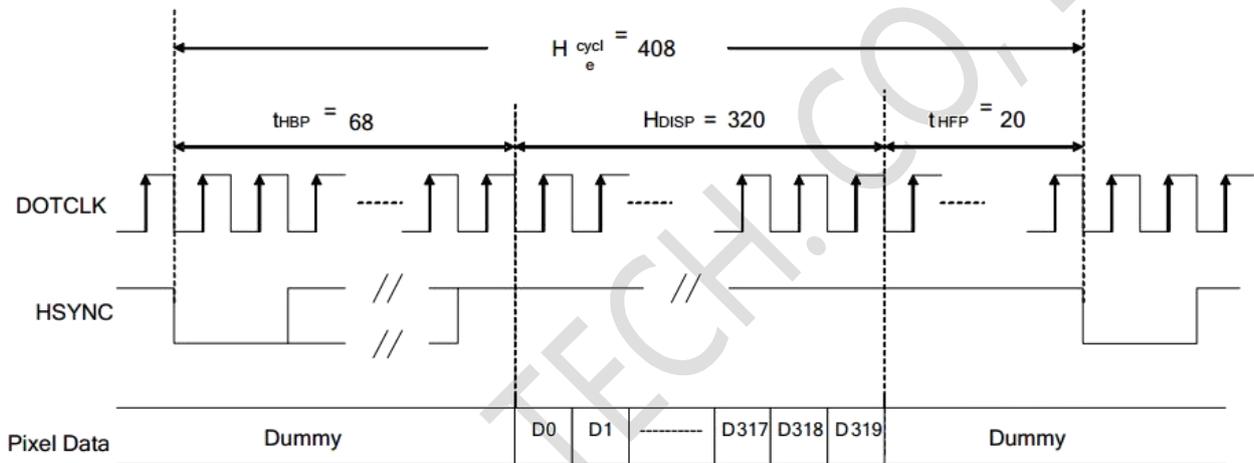
Pin No.	Symbol	Description
1	LEDK	Cathode of LED backlight.
2	LEDK	Cathode of LED backlight.
3	LEDA	Anode of LED backlight.
4	LEDA	Anode of LED backlight.
5	NC	Not Connection
6	NC	Not Connection
7	NC	Not Connection
8	RESET	System reset pin. - An active low pulse at this pin will reset the IC, Connect to VCC in normal operation
9	SPENA	CS : Chip select pin
10	SPCLK	Serial clock input
11	SPDAT	Data input/output pin in serial interface
12~19	DB0~DB7	Blue Data bus:B0~B7
20~27	DB8~DB15	Green Data bus:G0~G7
28~35	DB16~DB23	Red Data bus:R0~R7
36	HSYNC	Line Synchronization input
37	VSYNC	Frame/Ram Write Synchronization input
38	CLK	Dot-clock signal and oscillator source.
39	NC	Not Connection
40	NC	Not Connection
41	VDD	Power supply for digital voltage
42	VDD	Power supply for digital voltage
43	NC	Not Connection
44	NC	Not Connection
45	NC	Not Connection
46	NC	Not Connection
47	NC	Not Connection
48	X-	Touch panel pin
49	Y-	Touch panel pin
50	X+	Touch panel pin
51	Y+	Touch panel pin
52	DEN	Display enable pin from controller.
53	GND	Ground.
54	GND	Ground.

4 LCM Interface Timing

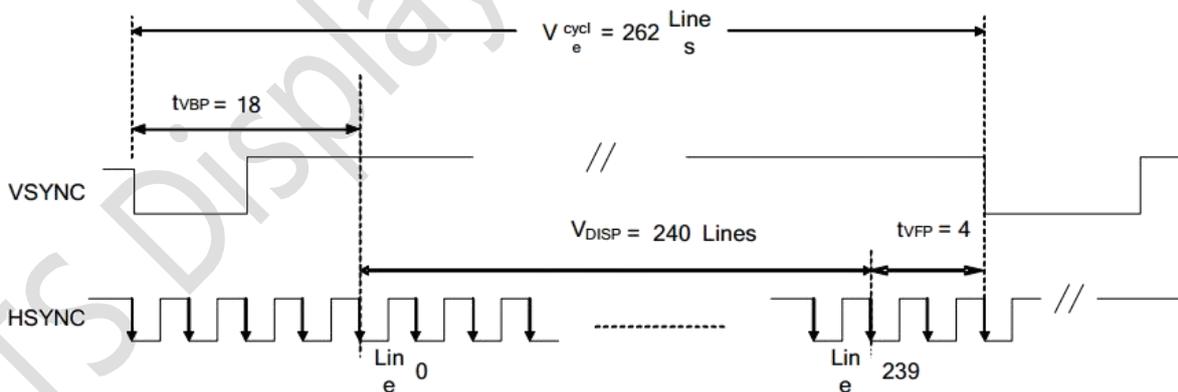
4.1 Reset Timing



4.2 RGB interface Timing



(a) Horizontal Data Transaction Timing



(b) Vertical Data Transaction Timing

Figure Data Transaction Timing in Parallel RGB (24 bit) Interface (SYNC Mode)

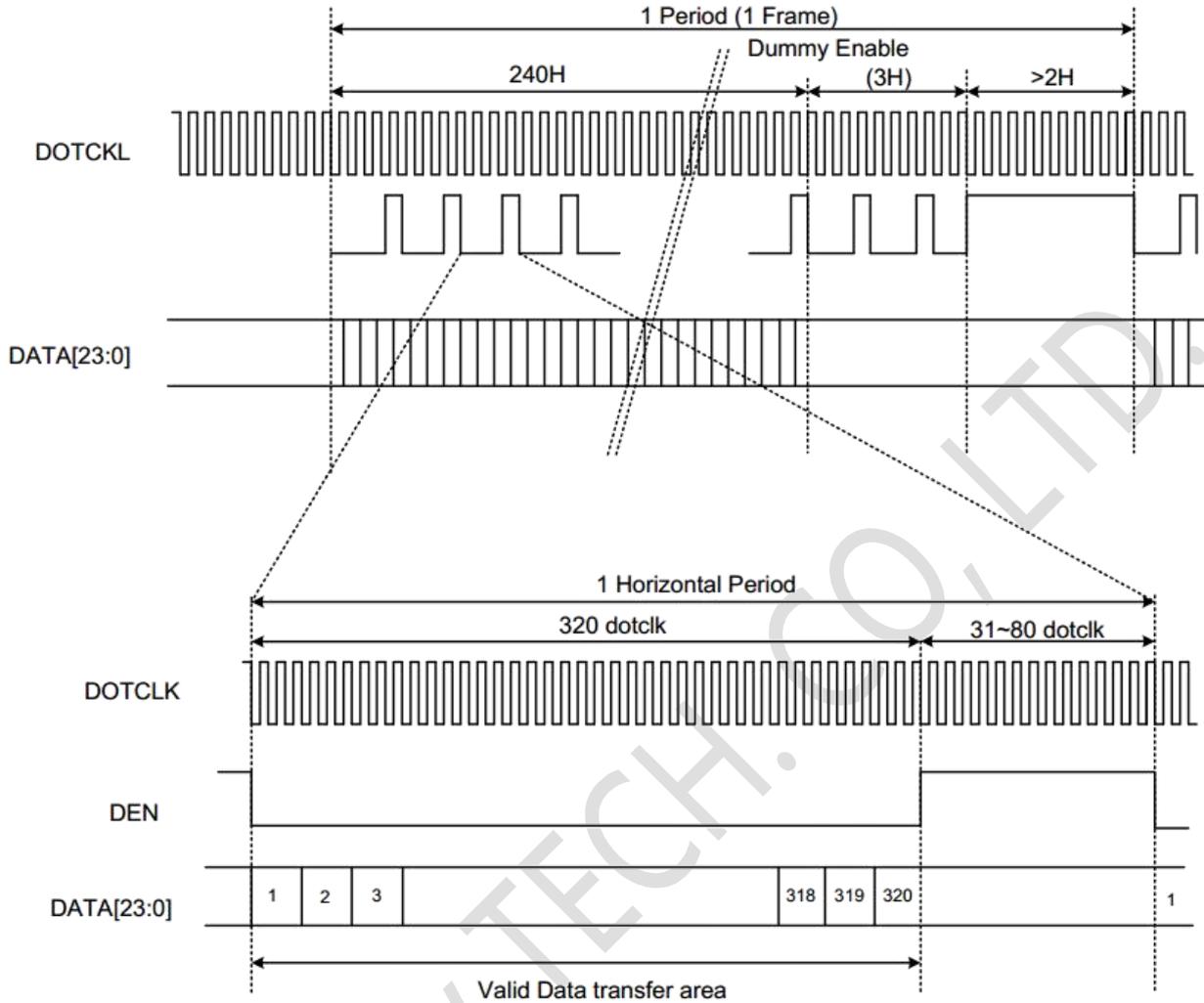
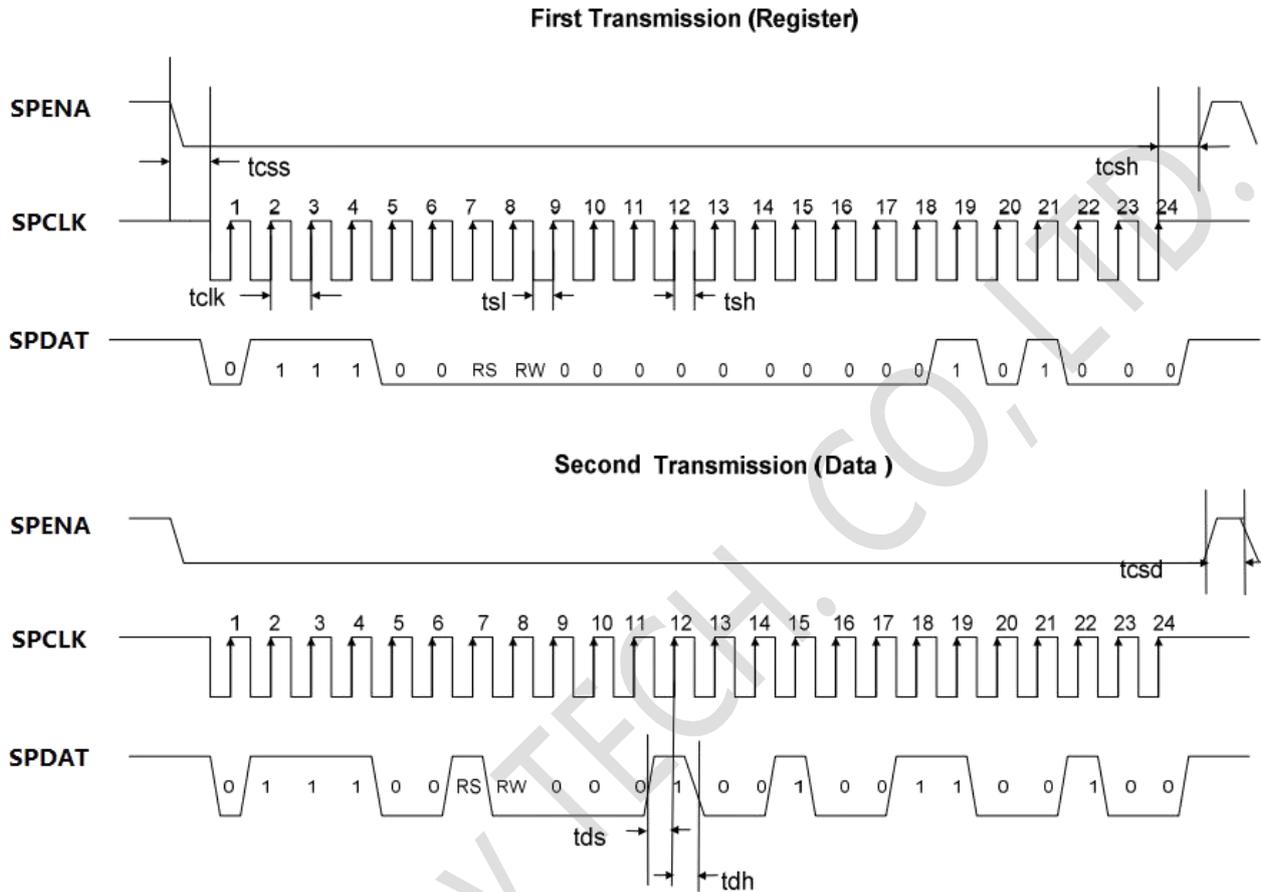


Figure Signal Timing in DE Mode

Characteristics	Symbol	Min.		Typ.		Max.		Unit
		24 bit	8 bit	24 bit	8 bit	24 bit	8 bit	
DOTCLK Frequency	fDOTCLK	-	-	6.5	19.5	10	30	MHz
DOTCLK Period	tDOTCLK	100	33.3	154	51.3	-	-	ns
Horizontal Frequency (Line)	fH	-	-	14.9		22.35		KHz
Vertical Frequency (Refresh)	fV	-	-	60		90		Hz
Horizontal Back Porch	tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Front Porch	tHFP	-	-	20	60	-	-	tDOTCLK
Horizontal Data Start Point	tHBP	-	-	68	204	-	-	tDOTCLK
Horizontal Blanking Period	tHBP + tHFP	-	-	88	264	-	-	tDOTCLK
Horizontal Display Area	HDISP	-	-	320	960	-	-	tDOTCLK
Horizontal Cycle	Hcycle	-	-	408	1224	450	1350	tDOTCLK
Vertical Back Porch	tVBP	-	-	18		-		Lines
Vertical Front Porch	tVFP	-	-	4		-		Lines
Vertical Data Start Point	tVBP	-	-	18		-		Lines
Vertical Blanking Period	tVBP + tVFP	-	-	22		-		Lines
Vertical Display Area	NTSC	-	-	240		-		Lines
	PAL			280(PALM=0)				
	PAL			288(PALM=1)				
Vertical Cycle	NTSC	-	-	262		350		Lines
	PAL			313				

4.3 Serial Timing Characteristics

- Write SPI



Note: The example writes “0x1264h” to register R28h.
 SPID connected to VSS.

Figure SPI interface Timing Diagram & Write SPI Example

Characteristics	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
Serial Clock Frequency	fclk	-	-	20	MHz
Serial Clock Cycle Time	tclk	50	-	-	ns
Clock Low Width	tsl	25	-	-	ns
Clock High Width	tsh	25	-	-	ns
Clock Rising Time	trs	-	-	30	ns
Clock Falling Time	tfl	-	-	30	ns
Chip Select Hold Time	tcsh	10	-	-	ns
Chip Select High Delay Time	tcshd	20	-	-	ns
Data Setup Time	tds	5	-	-	ns
Data Hold Time	tdh	10	-	-	ns

5 Absolute Maximum Ratings

PARAMETER	SYMBOL	MIN	MAX	UNIT
Supply Voltage (Analog)	VCC~GND	-0.3	5.0	V
Logic signal voltage(I/O)	IOVCC~GND	-0.3	4.0	V
Supply current (One LED)	I _{LED}	-	20	mA
Operating Temperature	Top	-20	70	° C
Storage Temperature	Tst	-30	80	° C
Operating Ambient Humidity	Hop	10	90%(Max 60° C)	RH

6 Electrical Characteristics

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Analog operating voltage	VCC	3.0	3.3	3.6	V
Logic operating voltage	IOVCC	3.0	3.3	3.6	V
Input Current	IDD	-	TBD	-	mA
Input Voltage 'H' level	VIH	0.7IOVCC	-	IOVCC	V
Input Voltage 'L' level	VIL	GND	-	0.3IOVCC	
Output Voltage 'H' level	VOH	0.8IOVCC	-	IOVCC	
Output Voltage 'L' level	VOL	GND	-	0.2IOVCC	

7 Backlight Characteristics

ITEM	SYMBOL	MIN	TYP	MAX	UNIT
Voltage for LED backlight	V _f	-	18.0	18.6	V
Current for LED backlight	I _f	-	15	20	mA
Power consumption	W _{bl}	-	384	-	mW
Uniformity	Avg	80	-	-	%
LED Life Time	-	30000	40000	-	

Note:

1. The LED life time is defined as the module brightness decrease to 50% original brightness at Ta=25°C, 60%RH ±5 %.
2. The life time of LED will be reduced if LED is driven by high current, high ambient temperature and humidity conditions.
3. Typical operating life time is an estimated data.
4. Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded .Functional operation should be restricted to the conditions described under normal operating conditions.

8 LCD optical specifications

ITEM	SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE
			MIN	TYP.	MAX		
Luminance	L	$I_L = 20\text{mA}$	-	500	-	Cd/m^2	
Contrast Ratio	CR	$\theta = 0^\circ$	200	300			
Response Time	T_{ON}	25°C		35	50	ms	
	T_{OFF}						
CIE Color Coordinate	Red	X_R	Viewing normal angle	0.609	0.639	0.669	
		Y_R		0.314	0.344	0.374	
	Green	X_G		0.264	0.294	0.324	
		Y_G		0.557	0.587	0.617	
	Blue	X_B		0.102	0.132	0.162	
		Y_B		0.106	0.136	0.166	
	White	X_W		0.282	0.312	0.342	
		Y_W		0.319	0.349	0.379	
Viewing Angle	Hor.	θ_{X+}	$CR \geq 10$	45	70	---	Degree
		θ_{X-}		45	70	---	
	Ver.	θ_{Y+}		25	50	---	
		θ_{Y-}		45	60	---	
Uniformity	Un		80			%	

Note 1: Ambient temperature = 25°C .

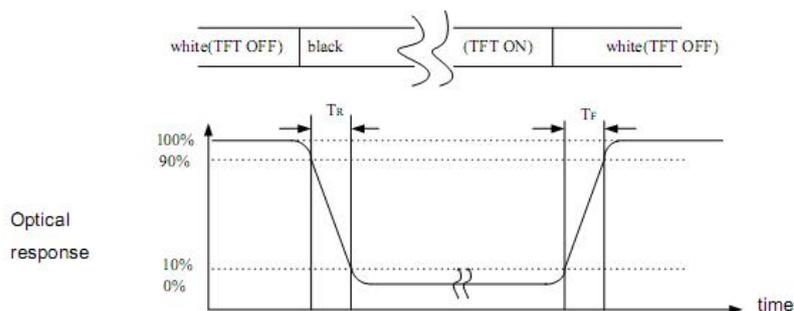
Note 2: To be measured with a viewing cone of 2° by Topcon luminance meter BM-5A.

Note 3: To be measured with Otsuta chromaticity meter LCF-2100M, CF only measure under C light simulation.

Note 4: shipping status is cell without polarizer. Transmittance of Specification is cell with polarizer. The tolerance of Transmittance is $\pm 10\%$.

Note 5: Definition of response time:

The output signals of TRD-100 are measured when the input signals are changed to “White” (falling time) and from “White” to “Black” (rising time), respectively. The interval is between the 10% and 90% of amplitudes. Refer to figure as below.

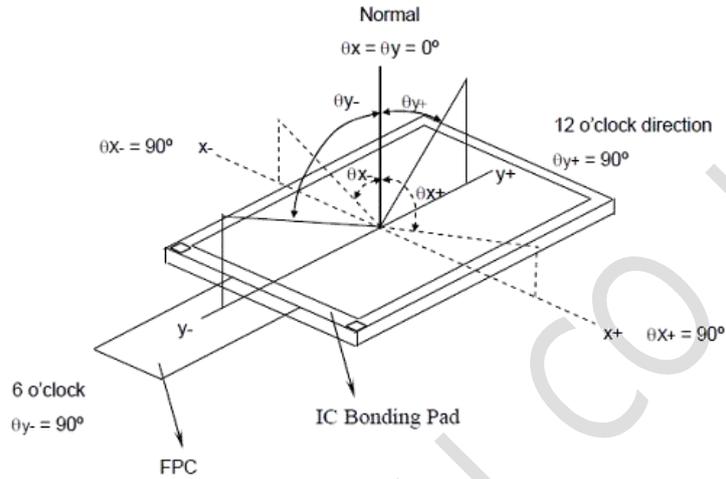


Note 6: Definition of contrast ratio:

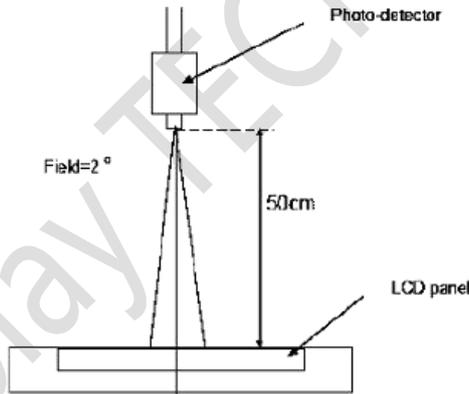
Contrast ratio is calculated by the following formula.

$$\text{Contrast ratio (CR)} = \frac{\text{Brightness on the "white" state}}{\text{Brightness on the "black" state}}$$

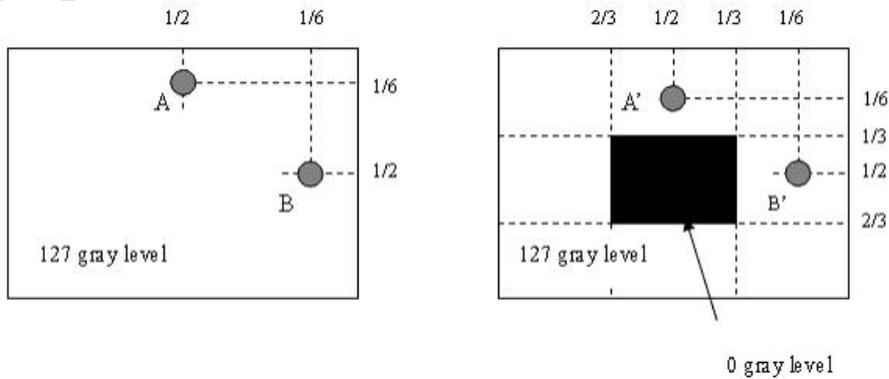
Note 7: Definition of viewing angle



Note 8: Optical characteristic measurement setup.



Note 9:



$|LA - LA'| / LA \times 100\% = 2\% \text{ max.}$, LA and LA' are brightness at location A and A'.

$|LB - LB'| / LB \times 100\% = 2\% \text{ max.}$, LB and LB' are brightness at location B and B'.

9 Resistive Touch Panel specifications

ITEM	VALUE			UNIT	REMARK
	Min	Typ	Max		
Linearity	-	-	1.5	%	Analog X and Y directions
Film		0.1888		mm	Double ITO film
Terminal Resistance	200	-	900	Ω	x
	200	-	900		y
Insulation Resistance	20	-	-	MΩ	DC 25V
Voltage	-	3	10	V	DC
Chattering	-	-	10	ms	100kΩ pull-up
Transparency	80	-	-	%	-
Operation Force	50	-	120	g	-
Endurance	1,000,000	-	-	Touches	100g Operation Force
	-	-	30,000	Slides	
Surface Hardness	3	-	-	H	-

10 Standard Specification for Reliability

10.1 Standard Specification for Reliability of LCD Module

No.	Item	Description
01	High temperature operation	The sample should be allowed to stand at 70°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
02	Low temperature operation	The sample should be allowed to stand at -20°C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
03	High temperature storage	The sample should be allowed to stand at 80°C for 240 hours under no-load condition, and then returning it to normal temperature condition, and allowing it stand for 2 hours.
04	Low temperature storage	The sample should be allowed to stand at -30°C for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.
05	Moisture storage	The sample should be allowed to stand at 60°C,90%RH MAX for 240 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.

06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles : -30°C for 30 minutes → normal temperature for 5 minutes → +80°C for 30 minutes → normal temperature for 5 minutes, as one cycle.
07	Packing vibration	Frequency range : 10Hz ~ 55Hz Amplitude of vibration : 1.5mm Sweep time: 12 min X,Y,Z 2 hours for each direction.
08	Packing drop test	According to ASTM-D-5327.
09	Electrical Static Discharge	Air: ±4KV 150pF/330Ω 5 times
		Contact: ±2KV 150pF/330Ω 5 time

10.2 Testing Conditions and Inspection Criteria

For the final test, the testing sample must be stored at room temperature for 24 hours. After the tests listed in Table 9.2, standard specifications for reliability will be executed in order to ensure stability.

No.	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

11 Specification of Quality Assurance

This standard of Quality Assurance confirms to the quality of LCD module products supplied by Tecenstar.

11.1 Quality Test

Before delivering, the supplier should conduct the following tests to confirm the quality of products.

- Electrical-Optical Characteristics: According to the individual specification to test the product.
- Appearance Characteristics: According to the individual specification to test the product.
- Reliability Characteristics: According to the definition of reliability on the specification for testing products.

11.2 Delivery Test

Before delivering, the supplier should conduct the delivery test.

- Test method: According to MIL-STD105E.General Inspection Level II take a single Time.
- The defects classify of AQL as following:
 - Major defect: AQL = 0.65
 - Minor defect: AQL = 2.5
 - Total defects: AQL = 2.5

11.3 Non-conforming Analysis & Deal With Manners

11.3.1 Non-conforming Analysis

- Purchaser should provide the data detail of non-conforming sample and the non-conforming.
- After receiving the data detail from purchaser, the analysis of non-conforming should be finished within two weeks.
- If the analysis can't be finished on time, supplier must notice purchaser 3 days in advance.

11.3.2 Disposition of non-conforming

- If any product defect be found during assembling, supplier must change the good for every defect after confirmation.
- Both supplier and customer should analyze the reason and discuss the disposition of non-conforming when the reason of nonconforming is not sure.

11.4 Agreement items

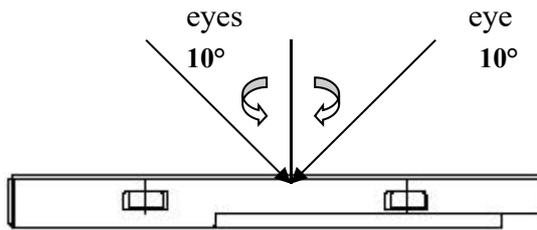
Both parties should negotiate together when the following problems happen.

- There is any problem of standard of quality assurance, and both sides should agree that it must be modified.
- There is any argument item which does not record in the standard of quality assurance.
- Any other special problem.

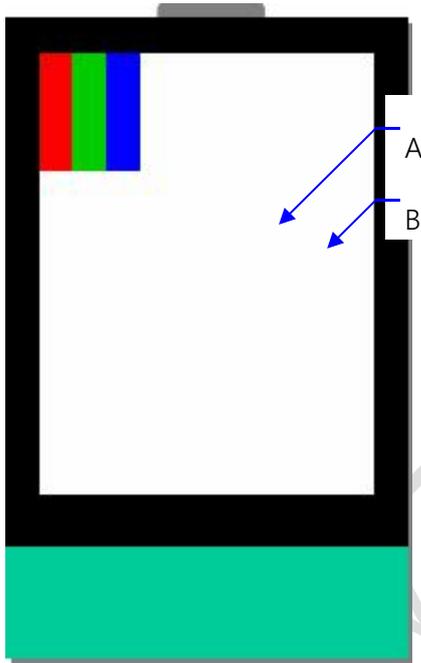
11.5 Standard of The Product Appearance Test

11.5.1 Manner of appearance test

- The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30±5cm.
- When test the model of transmissive product must add the reflective plate.
- The test direction is base on around 10° of vertical line.
- Temperature: 25±5°C Humidity: 60±10%RH



- Definition of area:

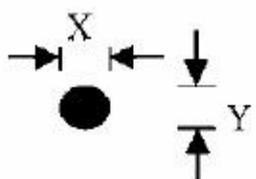
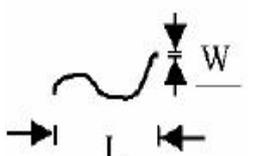


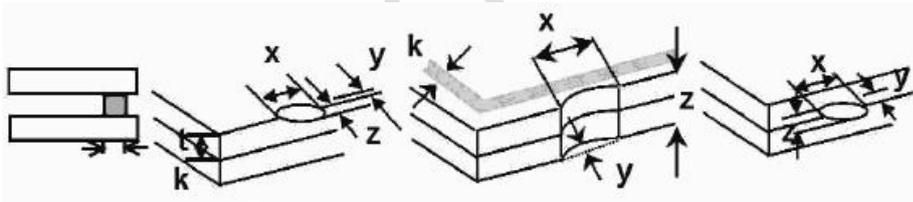
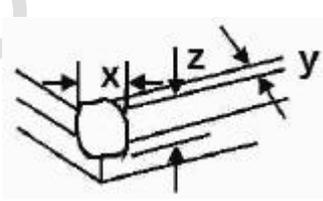
A: Viewing area B: Outside viewing area

11.5.2 Basic principle

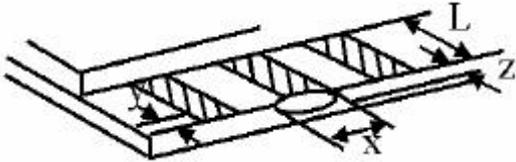
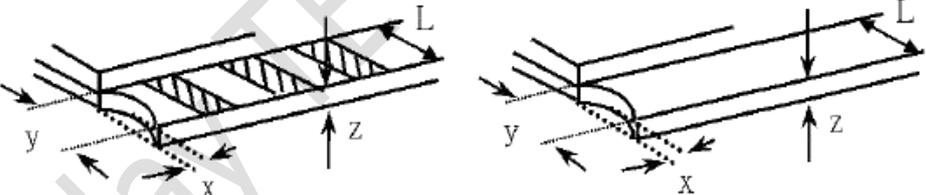
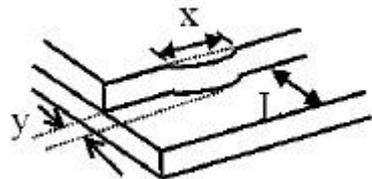
- When the standard can not be described, AQL will be applied.
- The sample of the lowest acceptable quality level must be negotiated by both supplier and customer when any dispute happened.
- New item must be added on time when it is necessary.

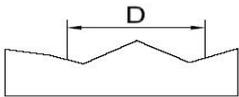
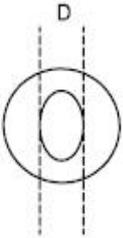
12 Inspection standards

NO.	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Flicker	0.65												
02	Black or White spots or Bright spots or Color spots on LCD (Display only)	2.1 White and black or color spots on display $\cong 0.25\text{mm}$, no more than Five spots. 2.2 Densely spaced: No more than three spots within 3mm.	2.5												
03	LCD and Touch Panel black spots, white spots, contamination (non - display)	3.1 Round type: As following drawing $\Phi = (X+Y) / 2$  <table border="1" data-bbox="821 1153 1356 1422"> <thead> <tr> <th>Size(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.20$</td> <td>2</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.25$</td> <td>1</td> </tr> <tr> <td>$0.25 < \Phi \leq 0.30$</td> <td>0</td> </tr> <tr> <td>$0.30 < \Phi$</td> <td>0</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two spots within 3mm.</p>	Size(mm)	Acceptable Q'ty	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.25$	1	$0.25 < \Phi \leq 0.30$	0	$0.30 < \Phi$	0	2.5
		Size(mm)	Acceptable Q'ty												
$\Phi \leq 0.10$	Accept no dense														
$0.10 < \Phi \leq 0.20$	2														
$0.20 < \Phi \leq 0.25$	1														
$0.25 < \Phi \leq 0.30$	0														
$0.30 < \Phi$	0														
3.2 Line type: (As following drawing)  <table border="1" data-bbox="726 1556 1356 1859"> <thead> <tr> <th>Length(mm)</th> <th>Width(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.02$</td> <td>Accept no dense</td> </tr> <tr> <td>$L \leq 3.0$</td> <td>$0.02 < W \leq 0.05$</td> <td rowspan="2">2</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.03 < W \leq 0.08$</td> </tr> <tr> <td>---</td> <td>$0.08 < W$</td> <td>Rejection</td> </tr> </tbody> </table> <p>* Densely spaced: No more than two lines within 3mm.</p>	Length(mm)	Width(mm)	Acceptable Q'ty	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.05$	2	$L \leq 2.5$	$0.03 < W \leq 0.08$	---	$0.08 < W$	Rejection	2.5
Length(mm)	Width(mm)	Acceptable Q'ty													
---	$W \leq 0.02$	Accept no dense													
$L \leq 3.0$	$0.02 < W \leq 0.05$	2													
$L \leq 2.5$	$0.03 < W \leq 0.08$														
---	$0.08 < W$	Rejection													

NO.	Item	Criterion	AQL										
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction	Size Φ (mm) Acceptable Q'ty	2.5									
			$\Phi \leq 0.20$ Accept no dense										
			$0.20 < \Phi \leq 0.50$ 1										
			$0.50 < \Phi \leq 1.00$ 0										
			$1.00 < \Phi$ 0										
			Total Q'ty 1										
05	Scratches	Follow NO.3 -2 Line Type.											
06	Chipped glass	<p>Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length</p> <p>6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels:</p> 	2.5										
		<table border="1"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </table>		z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	
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<p>⊙ Unit: mm ⊙ If there are 2 or more chips, x is the total length of each chip</p> <p>6.1.2 Corner crack:</p> 													
<table border="1"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </table>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$				
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NO.	Item	Criterion	AQL
08	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
09	Backlight elements	9.1 Illumination source flickers when lit. 9.2 Spots or scratches that appear when lit must be judged. Using LCD spot, lines and contamination standards. 9.3 Backlight doesn't light or color is wrong.	2.5 2.5 0.65
10	Bezel	Bezel must comply with product specifications.	2.5
11	PCB、COB	11.1 COB seal may not have pinholes larger than 0.2mm or contamination. 11.2 COB seal surface may not have pinholes through to the IC. 11.3 The height of the COB should not exceed the height indicated in the assembly diagram. 11.4 There may not be more than 2mm of sealant outside the seal area on PCB. And there should be no more than three places. 11.5 Parts on PCB must be the same as on the production characteristic chart, There should be no wrong parts, missing parts or excess parts. 11.6 The jumper on the PCB should conform to the product characteristic chart.	2.5 2.5 2.5 2.5 0.65 0.65
12	FPC	12.1 FPC terminal damage \cong 1/2 FPC terminal width and can not affect the function , we judge accept. 12.2 FPC alignment hole damage \cong 1/2 alignment area and can not affect the function , we judge accept.	2.5 2.5
13	Soldering	13.1 No cold solder joints, missing solder connections, oxidation or icicle. 13.2 No short circuits in components on PCB or FPC.	2.5 0.65

NO.	Item	Criterion	AQL																
07	Glass crack	<p>Symbols: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length</p> <p>7.2 Protrusion over terminal: 7.2.1 Chip on electrode pad:</p>  <table border="1" data-bbox="560 801 1236 958"> <thead> <tr> <th>y: Chip width</th> <th>x: Chip length</th> <th>z: Chip thickness</th> </tr> </thead> <tbody> <tr> <td>$y \leq 0.5\text{mm}$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </tbody> </table> <p>7.2.2 Non-conductive portion:</p>  <table border="1" data-bbox="560 1384 1236 1541"> <thead> <tr> <th>y: Chip width</th> <th>x: Chip length</th> <th>z: Chip thickness</th> </tr> </thead> <tbody> <tr> <td>$y \leq L$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </tbody> </table> <p>7.2.3 Substrate protuberance and internal crack</p>  <table border="1" data-bbox="890 1892 1324 2038"> <thead> <tr> <th>y: width</th> <th>x: length</th> </tr> </thead> <tbody> <tr> <td>$y \leq 1/3L$</td> <td>$X \leq a$</td> </tr> </tbody> </table> <p>⊙ If there chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙ If the product will be heat sealed by the customer, the alignment mark must not be damaged.</p>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$X \leq a$	2.5
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NO.	Item	Criterion	AQL										
15	Touch Panel(Fish eye, dent and bubble on film)	<table border="1"> <thead> <tr> <th>SIZE(mm)</th> <th>Acceptable Q'ty</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.2$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.2 < D \leq 0.4$</td> <td>5</td> </tr> <tr> <td>$0.4 < D \leq 0.5$</td> <td>2</td> </tr> <tr> <td>$0.5 < D$</td> <td>0</td> </tr> </tbody> </table>  	SIZE(mm)	Acceptable Q'ty	$\Phi \leq 0.2$	Accept no dense	$0.2 < D \leq 0.4$	5	$0.4 < D \leq 0.5$	2	$0.5 < D$	0	2.5
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$0.4 < D \leq 0.5$	2												
$0.5 < D$	0												
16	Touch Panel Newton ring	Newton ring dimension $\leq 1/2$ touch panel area and not affect font and line distortion($\leq 2.5\%$), it is acceptable.	2.5										
17	Touch Panel Linearity	Less than 2.5% is acceptable.	2.5										
18	LCD Ripple	Touch the touch panel , can not see the LCD ripple. Pen: R 1.0mm silicon rubber. Operation Force: 80g	2.5										
19	General appearance	19.1 Pin type must match type in specification sheet. 19.2 LCD pin loose or missing pins. 19.3 Product packaging must the same as specified on packaging specification sheet. 19.4 Product dimension and structure must conform to product specification sheet.	0.65 0.65 0.65 0.65										

13 Suggestions for using LCD modules

13.1 Handling of LCM

1. The LCD screen is made of glass. Don't give excessive external shock, or drop from a high place.
2. If the LCD screen is damaged and the liquid crystal leaks out, do not lick and swallow. When the liquid is attach to your hand, skin, cloth etc, wash it off by using soap and water thoroughly and immediately.
3. Don't apply excessive force on the surface of the LCM.
4. If the surface is contaminated, clean it with soft cloth. If the LCM is severely contaminated, use Isopropyl alcohol/Ethyl alcohol to clean. Other solvents may damage the polarizer. The following solvents is especially prohibited: water , ketone Aromatic solvents etc.
5. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
6. Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
7. Don't disassemble the LCM.
8. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded.
 - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
 - The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
9. Do not alter, modify or change the the shape of the tab on the metal frame.
10. Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
11. Do not damage or modify the pattern writing on the printed circuit board.
12. Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector
13. Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
14. Do not drop, bend or twist LCM.

13.2 Storage

1. Store in an ambient temperature of 5 to 45 C, and in a relative humidity of 40% to 60%. Don't expose to sunlight or fluorescent light.
2. Storage in a clean environment, free from dust, active gas, and solvent.
3. Store in antistatic container.

14 Packing

PARAMETER	Specification	Unit
Outside box	390(L) x 350(W) x 480(H)	mm
Inside box	330(L) x 185(W) x 110(H)	mm
Product quantity of Inside box	64	pcs
Total product quantity	64*8=512	pcs
Total weight	18.5±0.5	KG



TSD Display TECH. CO., LTD.